IN THE UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF E-FILING

I hereby certify that this correspondence has been e-filed with the U.S. Patent and Trademark Office on October 27, 2010.

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In re application of: Chamberlain et al.

Serial No.: 10/550,326 : Examiner: Roderick Tolentino

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For: Intelligent Data Storage and Processing

Using FPGA Devices

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Amendment and Response A

In response to the Office Action dated June 3, 2010, Applicant files the following amendment and remarks. Accompanying this response is a Petition for an Extension of Time (2 month) and the corresponding fee.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 11 of this paper.